	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	6817	(cpu or microprocessor or microcontroller or controller or processor)with handler	US-PGPUB; USPAT	2006/01/15 10:03
2	BRS	L2	233	1 same(policy or policies or rule or right or permission or privilege or acl)	US-PGPUB; USPAT	2006/01/15 10:04
3	BRS	L3	1315	(726/22,23,24 or 719/330).ccls.	US-PGPUB; USPAT	2006/01/15 10:04
4	BRS	L4	0	2 and 3	US-PGPUB; USPAT	2006/01/15 10:04
5	BRS	L5	285	1 same(alert$3 or alarm$3 or intrusion or attack$3 or hack$3 or malicious or suspicious or dos or ddos or (denial adj2 service))	US-PGPUB; USPAT	2006/01/15 10:05
6	BRS	L6	4	3 and 5	US-PGPUB; USPAT	2006/01/15 10:05